

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Protigal et al.

**Serial No.:** Not Yet Assigned

**Filed:** October 1, 2003

**For:** INTEGRATED CIRCUIT MODULE  
HAVING ON-CHIP SURGE CAPACITORS

**Confirmation No.:** Unknown

**Examiner:** Unknown

**Group Art Unit:** Unknown

**Attorney Docket No.:** 2269-2898.4US  
(88-0070.09/US)

**NOTICE OF EXPRESS MAILING**

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**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The present application is a continuation of application Serial No. 09/776,387, filed February 2, 2001, pending, which is a continuation of U.S. Patent Application Serial Number 08/965,741, filed November 7, 1997, now U.S. Patent 6,184,568, issued February 6, 2001, which is a continuation of U.S. Patent Application Serial Number 08/671,248, filed June 27, 1996, now U.S. Patent 5,687,109, which is a continuation of U.S. Patent Application Serial Number 08/178,716, filed January 10, 1994, now abandoned, which is a continuation of U.S. Patent Application Serial Number 08/034,001, filed March 19, 1993, which is now U.S. Patent 5,307,309, which is a continuation of U.S. Patent Application Serial Number 07/774,121, filed October 8, 1991, abandoned, which is a continuation of U.S. Patent Application Serial Number

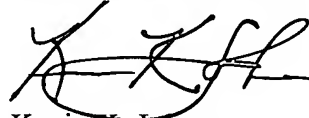
**Attorney Docket No. 2269-2898.4US**

07/291,294, filed December 27, 1988, now abandoned, which is a continuation-in-part of U.S. Patent Application Serial Number 07/200,673, filed May 31, 1988, now abandoned.

Pursuant to M.P.E.P. 2001.06(b), the Examiner is respectfully requested to consider the information of record in the eight prior applications, and to confirm in the first Office Action on the merits that such art has in fact been reviewed. A PTO-1449 or PTO/SB/08 form listing all of the information of record in the prior applications is enclosed herewith.

This Information Disclosure Statement is filed within three (3) months of the filing date of the above-identified application, and no certification pursuant to 37 C.F.R. § 1.97(c) or a fee pursuant to 37 C.F.R. 1.17(p) is required.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'K. K. Johanson', with a long horizontal line extending to the right.

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Date: October 1, 2003

KKJ/dlm:rmh

Enclosures: Form PTO-1449 or PTO/SB/08

Document in ProLaw

Form PTO-1449  <b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  <i>(Use several sheets if necessary)</i>	Docket Number (Optional) <b>2898.4US (88-070.09/US)</b>	Application Number <b>Not Yet Assigned</b>
	Applicant <b>Protigal et al.</b>	
	Filing Date <b>October 1, 2003</b>	Group Art Unit <b>Unknown</b>

### U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	3,460,010	08/1969	R.J. Domenico et al.			
	3,538,397	11/1970	S.P. Davis			
	3,566,203	02/1971	Maguire et al.			
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	4,266,282	05/1981	Henle et al.			
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	4,317,686	03/1982	Anand et al.			
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	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
	56-15065	02/1981	Japan				
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### OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

	McKenny, Verron D., "Good bits swapped for bad in 64-kilobit E-PROM," pps. 115-120 plus Cover and Table of Contents, <u>Electronics</u> , March 13, 1990.
	Natori et al., "FAM: 16.5: A 34ns 256K DRAM," pps. 232-233 plus Cover and Table of Contents, <u>IEEE International Solid-State Circuits Conference</u> , February 25, 1983.
	<u>Research Disclosure</u> , (3 pages), June 1985.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

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	4,419,812	12/1983	Topich			
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	4,587,639	05/1986	Aoyama et al.			
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						YES	NO
	58-64048	04/1983	Japan				
	58-77251	05/1983	Japan				
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	61-082458	04/1986	Japan				

### OTHER DOCUMENTS

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		"Decoupling Capacitor Structure to Reduce FET Output Driver Switching Noise," pps. 167-168, <u>IBM Technical Disclosure Bulletin</u> , Vol 30., No. 7, December 1987.
		"MOS Memory," (4 pages), <u>Toshiba Mos Memory Products Data Book</u> , April 1987.
		"Double-Traversing Pseudo-Folded-Bitline Design for Cross-Point Memory Cells," pps. 246-248, <u>IBM Technical Disclosure Bulletin</u> , Vol. 30, No. 11, April 1988.

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Form PTO-1449

# INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(Use several sheets if necessary)

Docket Number (Optional)  
2898.4US (88-070.09/US)

Application Number  
Not Yet Assigned

Applicant **Protigal et al.**Filing Date **October 1, 2003**Group Art Unit **Unknown**

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	4,720,737	01/1988	Shirato			

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		"New Memory Products 1987/1988," (4 pages), <u>NEC Electronics Inc. Supplement to 1986 Memory Products Data Book</u> , December 1987.
		Gillingham et al., "High-Speed, High-Reliability Circuit Design for Megabit DRAM," pps. 1171-1175 plus Table of Contents, <u>IEEE Journal of Solid-State Circuits</u> , Vol 26, No. 8, August 1991.
		"IBM's Response to Micron," (22 pages), December 8, 1995.

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	4,829,480	05/1989	Seo			
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		"On-Chip Decoupling Capacitor for Logic VLSI Chips," page 1280, IBM Technical Disclosure Bulletin, Vol. 30 No. 3, Aug 1987.

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	01-268032	10/1989	Japan				

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